WHAT IS CLAIMED IS:

1. An electronic system comprising:

a first integrated circuit comprising:

an input buffer coupled between a first supply terminal and a second supply terminal and further coupled to receive an input signal on a pad; and

a clamp diode coupled between the pad and the first supply terminal; and

a resistor coupled to the pad, and further coupled to receive an input signal.

2. The electronic system of claim 1 further comprising:

a second integrated circuit coupled to the resistor and to provide the input signal.

3. The electronic system of claim1 wherein the clamp diode has a anode and a cathode, the anode is coupled to the pad, and the cathode is coupled to the first supply terminal.

4. The electronic system of claim 3 wherein the first supply terminal is configured to receive a positive supply voltage, and the second supply voltage is configured to receive a ground supply.

5. The electronic system of claim 1 wherein the integrated circuit further comprises:

a pull-up output device coupled between the first supply terminal and the pad; and

a pull-down output device coupled between the pad and the second supply voltage.

6. The electronic system of claim 5 wherein the pull-up device has a gate coupled to a first predriver circuit, and the pull-down device has a gate coupled to a second predriver circuit.

7. The electronic system of claim 6 wherein the integrated circuit further comprises:

a core comprising a plurality of logic gates, and one of the logic gates is coupled to provide a signal to the first predriver.

8. The electronic system of claim 6 further comprising a switch coupled between the pad and clamp diode.

9. An electronic system comprising:

a first integrated circuit comprising:

an input buffer coupled between a first supply terminal and a second supply terminal and further coupled to receive an input signal on a pad; and

a series of clamp diodes coupled between the pad and the second supply terminal; and

a resistor coupled to the pad, and further coupled to receive an input signal.

10. The electronic system of claim 9 wherein the series of clamp diodes comprises four diodes.

11. The electronic system of claim 10 further comprising:

a second integrated circuit coupled to the resistor and to provide the input signal.

12. The electronic system of claim 9 wherein each of the clamp diodes in the series of clamp diodes has a anode and a cathode, the anode of one of the clamp diodes in the series of clamp diodes is coupled to the pad, and the cathode of one of the clamp diodes in the series of clamp diodes is coupled to the second supply terminal.

13. The electronic system of claim 12 wherein the first supply terminal is configured to receive a positive supply voltage, and the second supply voltage is configured to receive a ground supply.

14. The electronic system of claim 9 wherein the integrated circuit further comprises:

a pull-up output device coupled between the first supply terminal and the pad; and

a pull-down output device coupled between the pad and the second supply voltage.

15. The electronic system of claim 14 wherein the pull-up device has a gate coupled to a first predriver circuit, and the pull-down device has a gate coupled to a second predriver circuit.

16. The electronic system of claim 15 wherein the integrated circuit further comprises:

a core comprising a plurality of logic gates, and one of the logic gates is coupled to provide a signal to the first predriver.

17. An integrated circuit comprising:

a buffer having a first supply terminal, a second supply terminal, and an input;

a clamp circuit coupled between the input of the buffer and the first supply terminal of the buffer; and

a resistor coupled to the input of the buffer,

wherein the claim circuit comprises a clamp diode.

18. The integrated circuit of claim 17 wherein the resistor is further coupled to a pad.

19. The integrated circuit of claim 18 wherein the clamp circuit comprises one diode having an anode and a cathode, the anode coupled to the input of the buffer and the cathode coupled the first supply terminal.

20. The integrated circuit of claim 19 wherein the clamp circuit comprises a series of diodes each having an anode and a cathode, the anode of a first diode in the series of diodes coupled to the input of the buffer, and the cathode of a last diode in the series of diodes coupled to the first supply terminal.

21. The integrated circuit of claim 18 further comprising:

an output driver comprising:

a pull-up device coupled between the first supply terminal and the input of the buffer; and

a pull-down device coupled between the input of the buffer and the second supply terminal.